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/M.C./		PCT International Search Report dated October 10, 2003.
/M.C./		S. KALLEL et al.: "Throughput Performance of Memory ARQ Schemes," IEEE TRANSACTIONS ON VEHICULAR TECHNOLOGY, IEEE INC. NEW YORK, US, vol. 48, no. 3, May 1999, pp. 891-899, XP000906444, ISSN: 0018-9545

02/22/2008

EXAMINER: Initial if citation is considered, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

(Form PTO-1449 [6-4])

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /M.C./